

```
typedef struct EncoderObj }
                           /* IALG object MUST be first field */
   IALG_Obj ialgObj;
                           /* pointer to on-chip scratch memory */
   Int *workBuf;
   Int *historyBuf;
                           /* previous frame's data in ext mem */
{ EncoderObj ;
 Void algActivate (IALG_Handle handle)
    EncoderObj *inst = (EncoderObj *)handle;
                                                              1201
    /* copy history to beginning of on-chip working buf */
    memcpy(inst->workingBuf, inst->histBuf, HISTSIZE) ;
 Void encode (IALG_Handle handle,
                    Void *in[] , Void *out[] )
    EncoderObj *inst = (EncoderObj *) handle;
    /* append input buffer to history in on-chip workBuf */
    memcpy (inst->workBuf + HISTSIZE, in HISTSIZE);
    /* encode data */
    /* move history to beginning of workbuf for next frame */
    memcpy (inst->workBuf, inst->workingBuf + FRAMESIZE, HISTSIZE);
 Void algDeactivate (IALG_Handle handle)
    EncoderObj *inst = (EncoderObj *) handle;
                                                             1202
    /* save beginning of on-chip workBuf to history */
    memcpy (inst->histBuf, inst->workingBuf, HISTSIZE);
```



```
/* IALG object MUST be first field */
/* pointer to on-chip scratch memory */
/* expressed in words per frame */
                                                                                                                                                                                                      /* expressed in ms per frame */
                                                                                                                                                                                                                                                                                                                                                                                                             /* use default parameters */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    /* dual—access on—chip */
                                                                                                                                                                                                                                                     /* default parameters */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              _int_algAlloc(IALG_Params *algParams, IALG_Fxns **p, IALG_MemRec memTab[] )
                                                                                                                                                                                                                                                                                                                                                       EncoderParams *params = (EncoderParams *)algParams; if (params == NULL) {
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  memTab [1] .alignment = 1;
memTab [1] .type = IALG_PERSIST; 1304
memTab [1] .space = IALG_DARAM; 1304
                                                                                                                                                                                                                                                                                                                                                                                                                                                         memTab [0] .size = sizeof (EncoderObj);
memTab [0] .alignment = 1;
memTab [0] .type = IALC_PERSIST; 1301
memTab [0] .space = IALC_EXTERNAL; 1
                                                                                                                                                                                                                                                                                                                                                                                                          params = &ENCODERATTRS;
                                                                                                                                                                                                                                                    EncoderParams ENCODERATTRS = {5};
                                                                                                                                                                            typedef struct EncoderParams
                                                                         workBufLen;
                        ialgObj
*workBuf;
typedef struct EncoderObj
IALG_Obj ialgObj
                                                                                                                                                                                                      Int frameDuration;
                                                                                                         EncoderObj;
```

return (2);



```
typedef struct EncoderStatus }
                              Bool voicePresent; /* voice in current frame? */
                            { EncoderStatus;
        FIG. 14
                            typedef enum {EncoderGetStatus, ... { EncoderCmd;
                           Void algControl (IALG_Handle handle,
                                            IALG_Cmd cmd, IALG_Status *status)
                              EncoderStatus *sptr = (EncoderStatus *)status;
                              switch ((EncoderCmd)cmd) {
                                  case EncoderGetStatus:
                                     sptr->voicePresent = ...;
                                  case EncoderSetMIPS:
    FIG. 15
typedef struct EncoderObj }
     IALG_Obj
                 ialg0bj
                                                   /* IALG object MUST be first field */
     Int
                 *workBuf:
     Int
                 workBufLen;
{ EncoderObj;
Int algFree(IALG_Handle handle, IALG_MemRec memTab[] )
     EncoderObj *inst = (EncoderObj *)handle;
     algAlloc(NULL, memTab) ;
                                                   /* get default values first */
     memTab[1] .size = inst->workBufLen * sizeof(Int) ;
     memTab[1] .base = (Void *)inst->workBuf;
     return(2);
Int algAlloc(IALG_Params *params, IALG_MemRec memTab[] )
     memTab[0] .size = sizeof (EncoderObj);
     memTab[0] .alignment = 1;
     memTab[0] .type = IALG_PERSIST;
     memTab[0] .space = IALG_EXTERNAL;
     memTab[1] .size = 80:
                                                   /* 10ms @ 8KHz */
     memTab[1] .alignment = 1;
                                                   /* no alignment */
     memTab[1] .type = IALG_PERSIST;
     memTab[1] .space = IALG_DARAM;
                                                   /* dual-access on-chip */
     return (2);
```

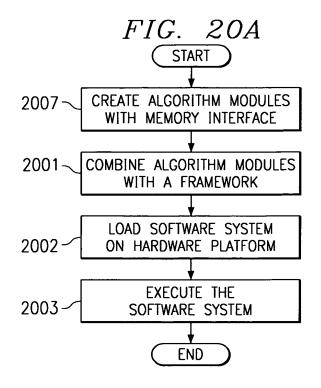


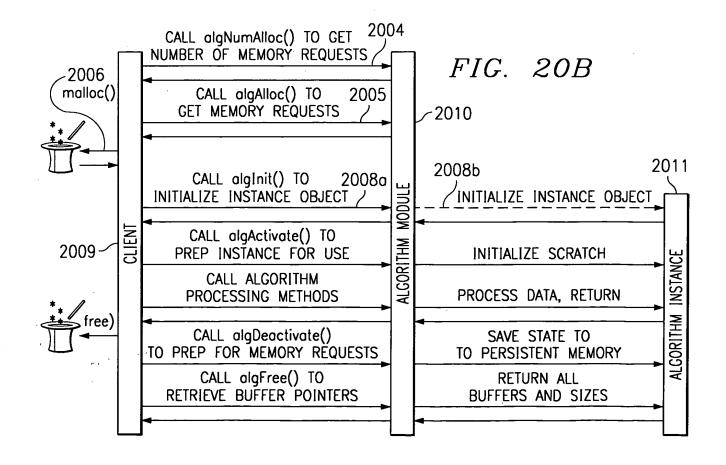
```
typedef struct EncoderObj }
                                                /* IALG object MUST be first field */
      IALG_Obj
                  ialg0bj
                  workBuf;
                                                /* pointer to on-chip scratch memory */
      Int
                  workBufLen;
                                                /* workBuf length (in words) */
      Int
{ EncoderObj ;
Int algInit(IALG_Handle handle,
          IALG_MemRec memTab[], IALG_Handle p, IALG_Params *algParams)
{
      EncoderObj *inst = (EncoderObj *)handle;
      EncoderParams *params = (EncoderParams *)algParams;
      if (params == NULL) {
          params = &ENCODERATTRS;
                                                /* use default parameters */
      }
      inst->workBuf = memTab[1] .base;
      inst->workBufLen = params->frameDuration * 8;
      return (IALG_EOK);
```

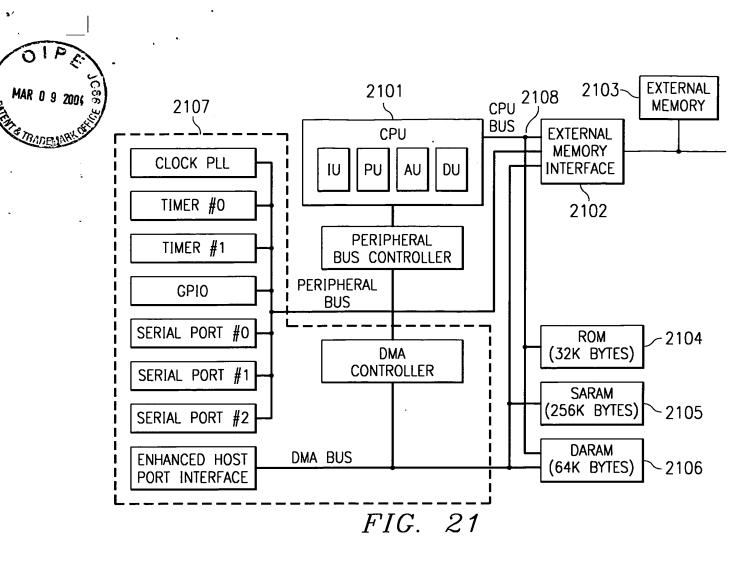


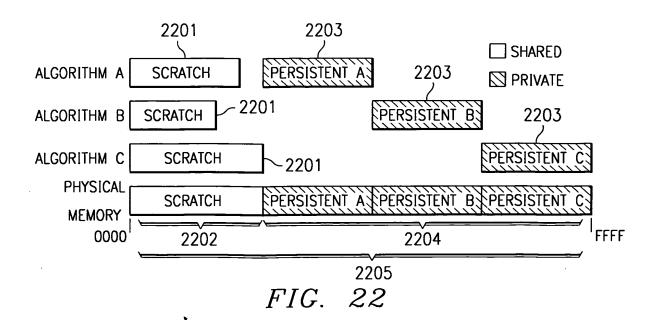
```
/* max number of my memory requests */
                #define NUMBUF 3
FIG.
          18 extern IALG_Fxns +subAlg; /+ sub-algorithm used by this alg +/
               √ Int algNumAlloc(Void)
                   return (NUMBUF + subAlg->algNumAlloc());
                Int algAlloc(const IALG_Params *p, struct IALG_Fxns **pFxns,
                          IALG_MemRec memTab)
                   Int n;
                   /* initialize my memory requests */
                   /* initialize sub-algorithm's requests */
                   n = subAlg - > algAlloc(..., memTab);
                   return (n + NUMBUF);
                              TYPES AND CONSTANTS
                       #define IRTC_ENTER
      FIG.
                19
                       #define IRTC_CLASS1
                       #define IRTC_CLASS2
                       #define IRTC_CLASS3
                       #define IRTC_CLASS4
                       #define IRTC_CLASS5
                       #define IRTC_CLASS6
                       #define IRTC_CLASS7
                           Handle to module's trace instance object
                       typedef struct IRTC_Obj *IRTC_Handle;
                         typedef LgUns IRTC_Mask;
                         typedef struct IRTC_Fxns }
                          Void
                                   *implementationId;
                                   (*rtcBind) (LOG_Obj *log);
                          IRTC_Mask (*rtcGet) (IRTC_Handle);
                                   (*rtcSet) (IRTC_Handle, IRTC_Mask mask);
                        IRTC_Fxns;
```



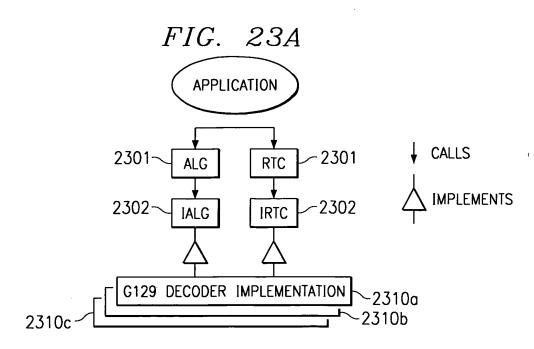


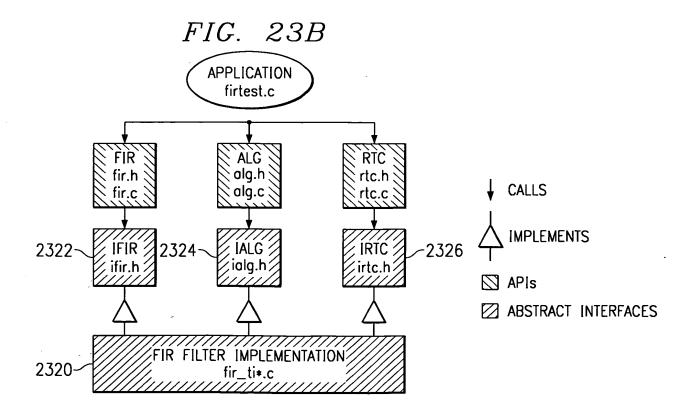


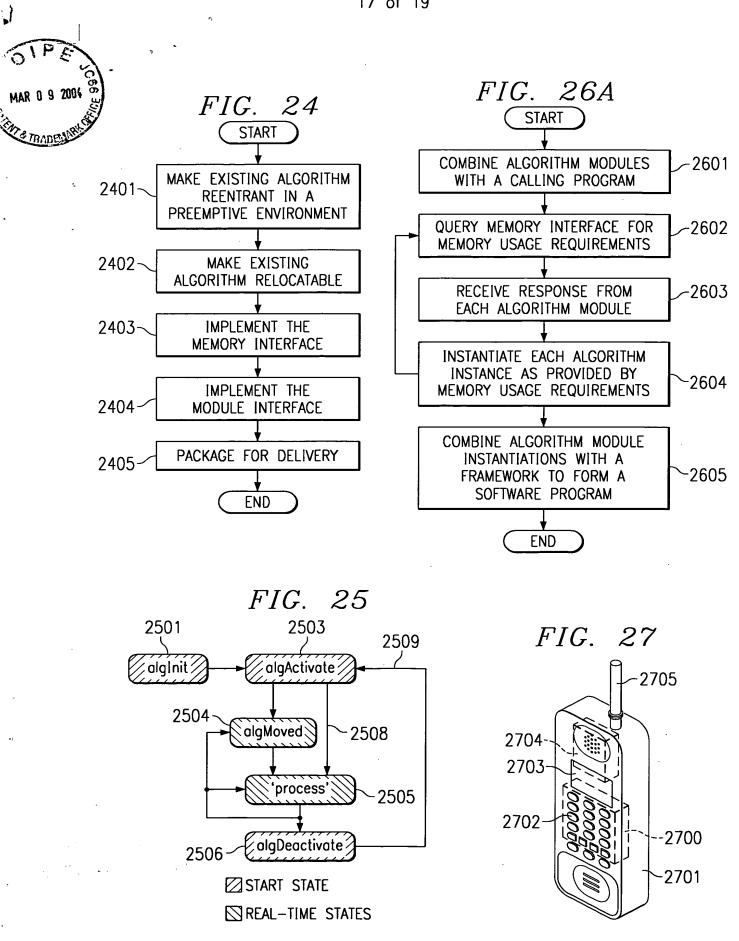




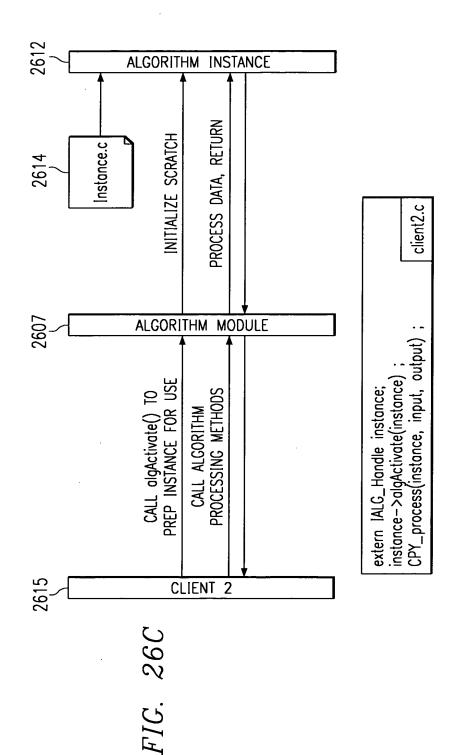






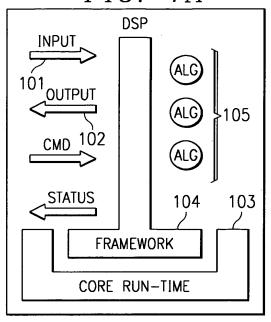


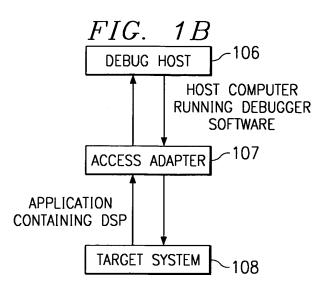


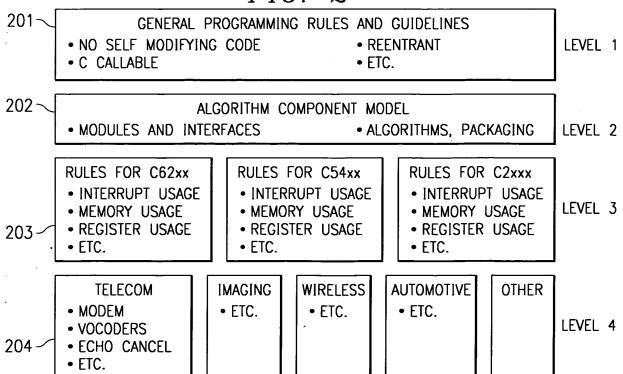




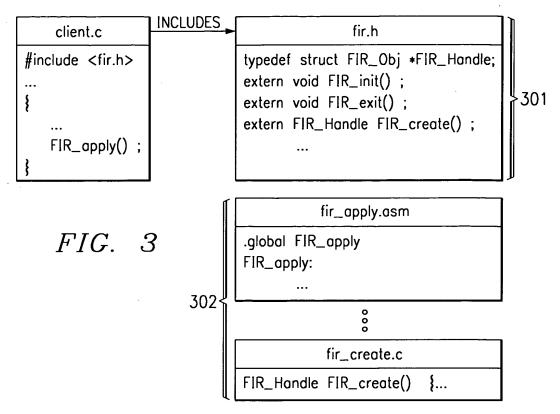


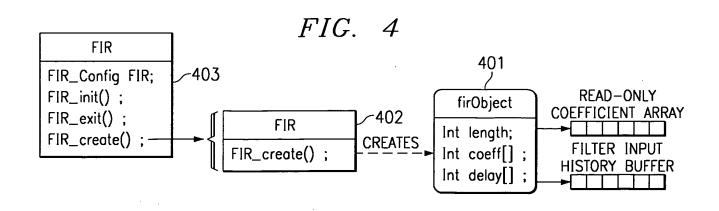


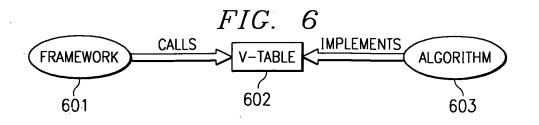












```
TI-29688
                                    3 of 19
ETRANE!
               void FIR_init (void)
   501
                                               FIG. 5
               void FIR_exit (void)
               typedef FIR_Params \ /* FIR_Obj creation parameters */
                   int frameLen;
                                        /* input/output frame length */
                   int *coeff;
                                        /* pointer to filter coefficients */
               FIR_Params ;
               FIR_Params FIR_PARAMS = \ 64, NULL \ ; /* default parameters */
  505
               typedef struct FIR_Obj { /* FIR_Obj definition */
                   int hist [16];
                                        /* previous input value */
                   int frameLen;
                                        /* input frame length */
                   int *coeff;
               { FIR_Obj ;
         FIR_Handle FIR_create (FIR_Obj *fir, const FIR_Params *params)
            if (fir != NULL) }
               if (params == NULL) \{ /* \text{ use defaults if params is NULL } */
                 params = \&FIR\_PARAMS;
               fir->frameLen = params->frameLen ;
  502
               fir->coeff = params->coeff ;
               memset(fir->hist, 0, sizeof (fir->hist));
            return (fir);
         void FIR_delete (FIR_Handle fir)
         void FIR_apply (FIR_Handle fir, int in[], int out[])
            int i :
            /* filter data using coefficients fir->coeff and
                 history fir->hist */
            for (i = 0; i < fir -> frameLen; i++)
                 out [i] = filter(in[i], fir->coeff, fir->hist);
```



```
'* 'sizeof' memory request in MAUs (minimum address—able unit) */
'* alignment requirement (in MAUs) */

    eff-chip data memory (accessed sequentially) *,
    eff-chip data memory (accessed randomly) */

                                                                                  /* memory record index of instance object */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               block 1, if independent blocks required */
                                                                                                                                                                     /* unspecified error return status code */
                                                                                                            /* minimum "system" IALG_Cmd value *,
                                                      * default number of memory records */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         dual access on-chip data memory */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                single access on—chip data memory is block 0, equivalent to IALG_SARAM */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             dual access on—chip data memory */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            /* base address of allocated buf */
                                                                                                                                        /* successful return status code */
                                                                                                                                                                                                                                                                                     /* write-once persistent memory */
                                                                                                                                                                                                                                                                                                                                           /* program memory space bit */
                                                                                                                                                                                                                                                                                                                                                                 /* external memory space bit */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            external program memory */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     internal program memory *,
                                                                                                                                                                                                                                                         /* persistent memory *,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     * memory attributes */
                                                                                                                                                                                                                              /* scratch memory */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             /* allocation space */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           IALG_MXTRN,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      IALC_EPROG = IALC_MPRÒG | IALI
IALC_IPROG = IALC_MPROG,
IALC_ESDATA = IALC_MXTRN + 0,
                                                                                                                                                                                                                                                                                                                                                                                                                         ==== IALG_MemSpace =====
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                typedef enum IALG_MemSpace
                                                                             #define IALG_OBJMEMREC 0
#define IALG_SYSCMD 256
-#define IALG_EOK 0
                                                                                                                                                                                                    typedef enum IALG_MemAttrs
* TYPES AND CONSTANTS */
                                                                                                                                                                                                                                                                                                                                        714  #define IALG_MPROG 0x0008  #define IALG_MXTRN 0x0010
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                typedef struct IALG_MemRec
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         IALG_MemSpace space;
IALG_MemAttrs attrs;
                                                 #define IALG_DEFMEMRECS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | ALG_DARAM1 = 1,
| ALG_SARAM = 2,
| ALG_SARAM0 = 2,
| ALG_SARAM1 = 3
                                                                                                                                                                  -#define IALG_EFAIL -1
                                                                                                                                                                                                                                                                             IALG_WRITEONCE
IALG_MemAttrs;
                                                                                                                                                                                                                             IALG_SCRATCH, IALG_PERSIST,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                nt alignment;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ALG_DARAMO
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            IALG_MemSpace;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            Void *base;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     IALG_MemRec;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      Int size;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            710
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             708
                                                                                                                                                                                                                                                    709
```



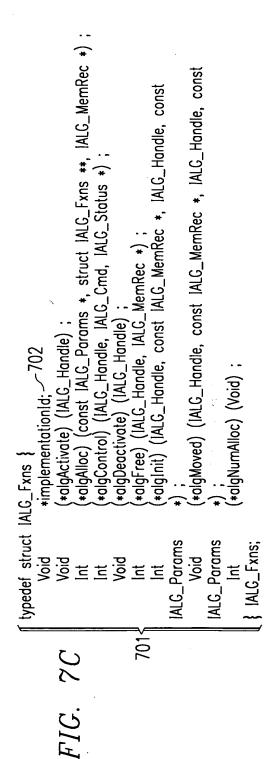
FIG. 7B

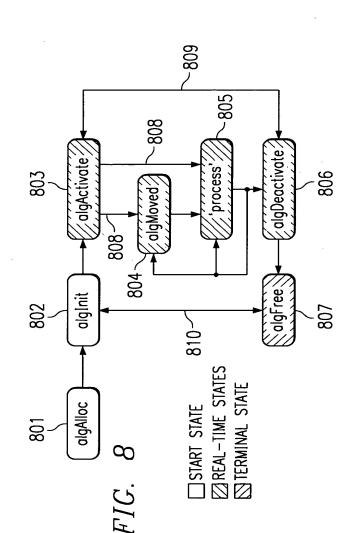
```
* Algorithm instance object definition
 * All XDAIS algorithm instance objects *must* have this structure as their first element.
 * However, they do not need to initialize it; initialization of this sub-structure is done by
 * the "framework".
typedef struct IALG_Obj {
    struct IALG_Fxns *fxns; } 705
{ IALG_Obj;
 * Handle to an algorithm instance object
 typedef struct IALG_Obj *IALG_Handle; $\rightarrow 707
 * Algorithm instance creation parameters
 * All XDAIS algorithm parameter structures *must* have a this as their first element.
typedef struct IALG_Params }
     Int size; /* number of MAUs (i.e. the 'sizeof') the structure */ 703
{ IALG_Params;
  * Pointer to algorithm specific status structure

    All XDAIS algorithm status structures *must* have this as their first element.

typedef struct IALG_Status }
     Int size; /* number of MAUs (i.e. the 'sizeof') the structure */
{ IALG_Status;
 * Algorithm specific command. This command is used in conjunction with IALG_Status to
 * get and set algorithm specific attributes via the algControl method.
typedef unsigned int IALG_Cmd;
```









```
client ()
     FIR_Params stdParams;
     FIR_TI_Params tiParams;
     stdParams = FIR_PARAMS;
                                                  /* initialize all fields to defaults */
                                                  /* initialize selected parameters */
     stdParams.coeff = ...;
     fxns->algAlloc(&stdParams, ...);
                                                  /* pass parameters to algorithm */
     tiParams = FIR_TI_PARAMS;
                                                  /* initialize all fields to defaults */
     tiParams.coeff = ...;
                                                  /* initialize selected parameters */
                                                  /* pass parameters to algorithm */
     fxns->algAlloc(&tiParams, ...);
Int FIR_TI_algAlloc(IALG_Params *clientParams, ...)
     FIR_TI_Params params = FIR_TI_PARAMS;
     /* client passes in parameters, use them to override defaults */
     if (clientParams != NULL) {
     memcpy(&params, clientParams, clientParams->size);
     /* use params as the complete set of parameters */
Ì
```

```
Void FIR_apply (FIR_Handle alg, Int *in[], Int *out[] )
{
    /* do app specific initialization of scratch memory */
    if (alg->fxns->ialg.algActivate != NULL) {
        alg->fxns->ialg_algActivate(alg) ;
    }
    /* filter data */
    alg->fxns->filter(alg, in, out) ;

    /* do app specific store of persistent data */
    if (alg->fxns->ialg.algDeactivate != NULL) {
        alg->fxns->ialg.algDeactivate(alg) ;
    }
}
```



```
#define MAXMEMRECS 16
             typedef struct ALG_Obj }
                                                 /* algorithm functions */
                IALG_Fxns
                             fxns;
             ALG_Obj;
             IALG_Handle ALG_create (IALG_Fxns *fxns, IALG_Params *params)
                IALG_MemRec
                               memTab [MAXMEMRECS];
                IALG_Handle
                               alg = NULL;
                Int
                               n;
                if (fxns->algNumAlloc () <= MAXMEMRECS) {
                   n = fxns - > algAlloc(params, memTab);
                   if (allocMemory(memTab, n)) {
                alg = (IALG\_Handle)memTab[0] .base;
1001
                alg->fxns = fxns;
                if (fxns->algInit(alg, memTab, params) != IALG_EOK) {
                   fxns->algFree(alg, memTab) ;
                   freeMemory(memTab, n) ;
                   alg = NULL;
          return (alg);
       Void ALG_delete (IALG_Handle alg)
          IALG_MemRec memTab [MAXMEMRECS] ;
1002
          Int n;
          n = alg->fxns->algFree(alg, memTab);
          freeMemory(memTab, n);
```